



EMC Distinguished Lecture

**Eine Veranstaltung des
deutschen Chapters der IEEE
EMC Society!**

Herzlich eingeladen sind alle, die an unseren Aktivitäten interessiert sind und den Kontakt zu unserem Chapter suchen.

EMC Distinguished Lectures sind EMV-spezifische Seminare von international anerkannten Experten aus Industrie, Hochschulen und Behörden. Die Vortragenden werden durch die IEEE EMC Society ausgewählt und unterstützt.

Treffen Sie Kollegen/-innen und bringen Sie sich auf den aktuellsten Stand von Technik und Forschung!

Dr. Bhyrav Mutnury

Senior Distinguished Engineer at Dell EMC
Austin, Texas, United States

“AI/ML Concepts for Signal and Power Integrity”

Datum: 10th Dec. 2021
Zeit: 16:00 -17:00 PM (UTC+1)
Online: Zoom Meeting

(<https://tuhh.zoom.us/j/3212339859?pwd=RzVsdVBHdHVRY0hRMINWdXNRaHVmUT09>,
or check it from <https://site.ieee.org/germany-emc/activities/events>)

Kontakt:

Prof. Dr. sc. techn. Christian Schuster
Institut für Theoretische Elektrotechnik
Hamburg University of Technology (TUHH)
Blohmstr. 15, 21079 Hamburg
Tel: 040 42878 3116
E-Mail: schuster@tuhh.de
WWW: www.tet.tuhh.de

Hints: an email reminder will be sent out in case of any changes. Please **sign up in advance** (E-mail: cheng.yang@tuhh.de)



EMC Distinguished Lecture by Dr. Bhyrav Mutnury

AI/ML Concepts for Signal and Power Integrity

Abstract: In today's world, artificial intelligence (AI) and machine learning (ML) approaches are becoming pervasive and affecting every aspect of life. While their influence in day-to-day life can be felt easily, their value and importance in designing complex systems is known to a lesser extent. In this presentation, AI/ML techniques used at Dell EMC to design high-speed complex servers are discussed. The importance of modeling and simulations in signal integrity, and how AI/ML techniques are used for design optimization is also discussed using examples. The talk discusses the advantages and limitations of a few major AI/ML techniques in detail.



EMC Distinguished Lecture by Dr. Bhyrav Mutnury

AI/ML Concepts for Signal and Power Integrity

Biography: Dr. Bhyrav Mutnury is a Senior Distinguished Engineer and Global SI Leader at Infrastructure Solutions Group at Dell Technologies where he is responsible for storage, network, rack, and blade server designs. He is driving the next generation high-speed interfaces and modeling methodologies at Dell. He has more than 21 years of progressive experience in system design with strong focus on electrical modeling, analysis and optimization of complex high speed servers.

Dr. Mutnury has authored and co-authored more than 80 refereed publications in various IEEE and non IEEE conferences. He has filed more than 180 invention disclosures. He is an IEEE Fellow and the recipient of IEEE EMC Technical Achievement Award.

Dr. Bhyrav Mutnury received the M.Sc. degree in electrical engineering, in 2002, and the Ph.D. in electrical engineering, in 2005 from the Georgia Institute of Technology, Atlanta, GA, USA.